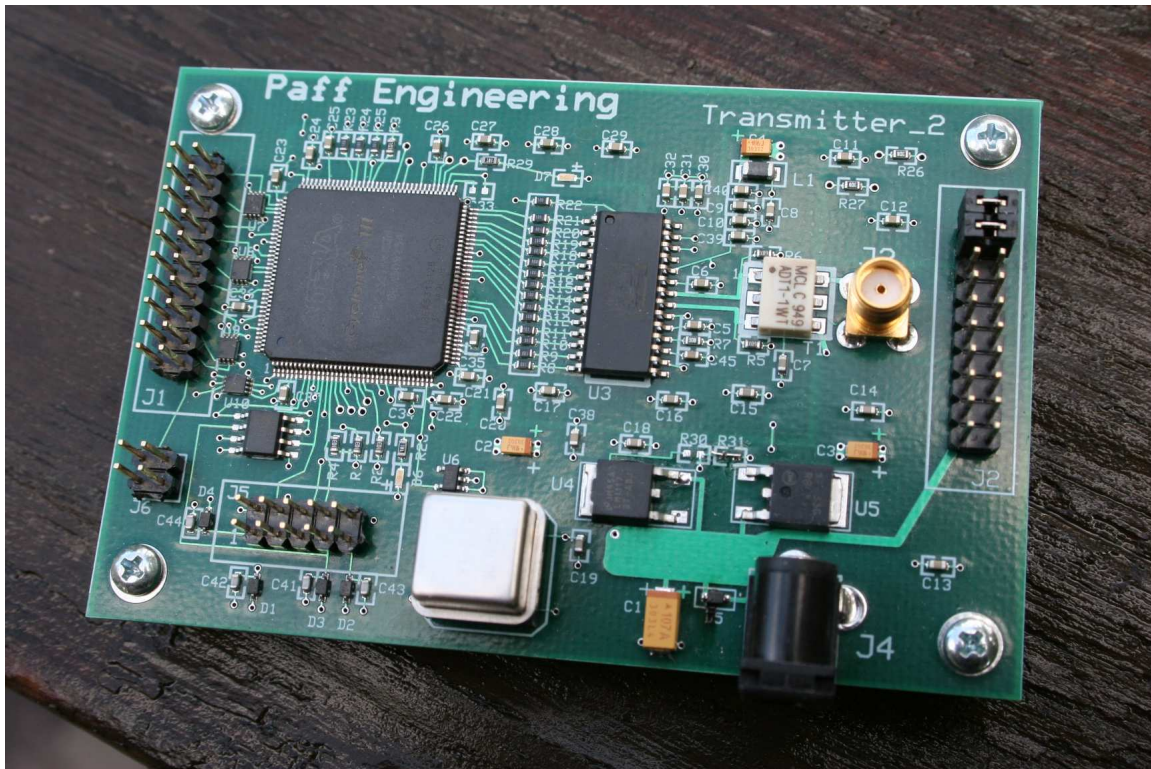


# Modulator Evaluation Board

Apr. 2010

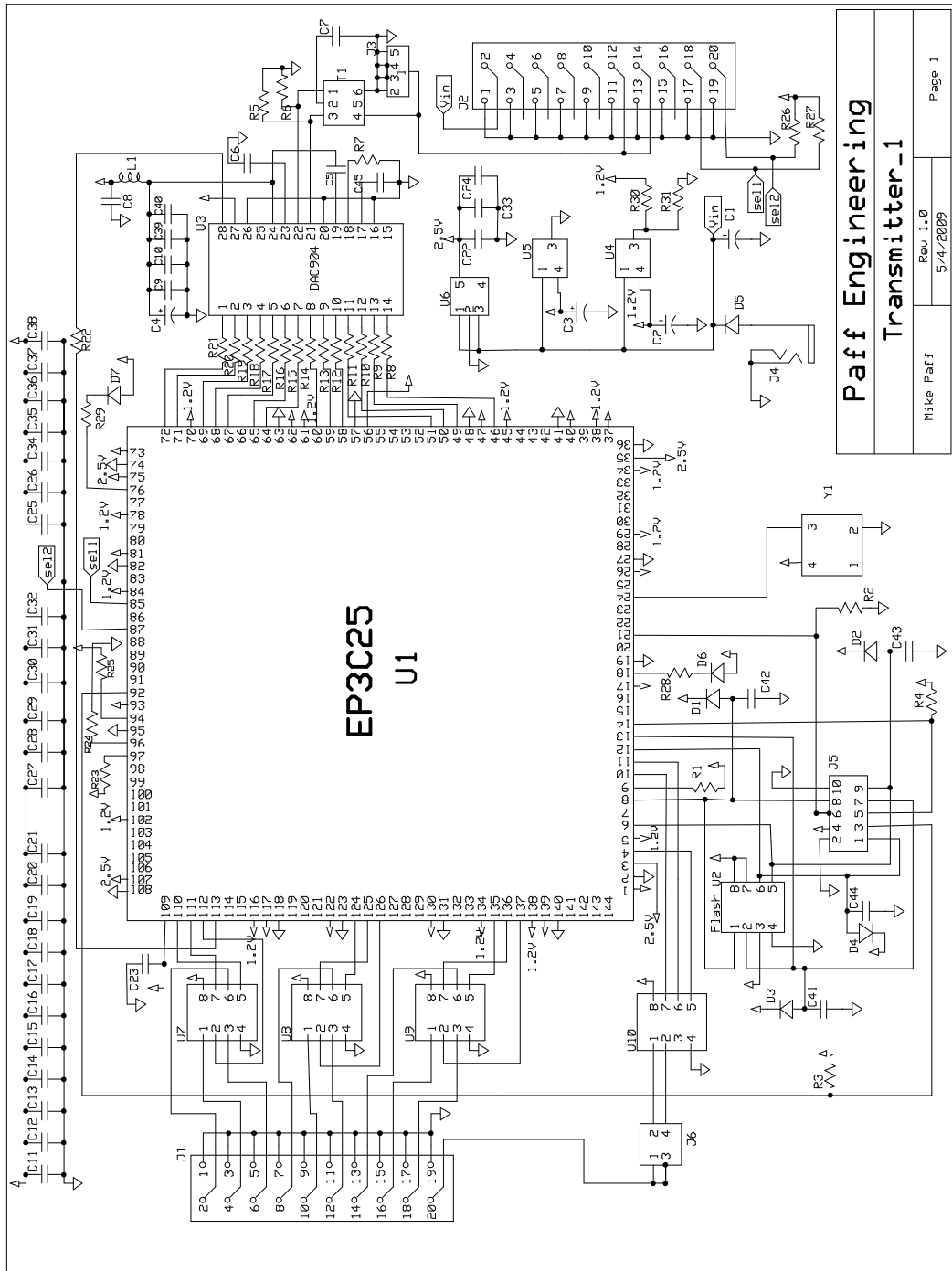


## Features

- Low Power Cyclone III technology (EP3C25E144)
- 14 bit DAC904 from Texas Instruments.
- Post DAC transformer insures optimum spectral purity.
- Non Volatile Serial Programming device (EPCS16).
- Buffered IO.
- Input and Output headers to simplify interface testing.
- On board power conditioning requires +5 VDC input.
- Available with or without modulator programmed into FPGA
- Flexible clock frequencies.
- Supports ATSC, SOQPSK, OFDM

This board integrates a small, low power Cyclone III FPGA device with a single, high speed, 14 bit DAC to produce a modulated IF signal used for digital communications. The FPGA may be configured to conform to one of several standards, with a user defined set of operating conditions.

Contact Mike Paff 650 941 2954 (mpaff@paffengineering.com) for more information



**Paff Engineering**

**Transmitter\_1**

Rev. 1.0

Mike Paff

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5/4/2009