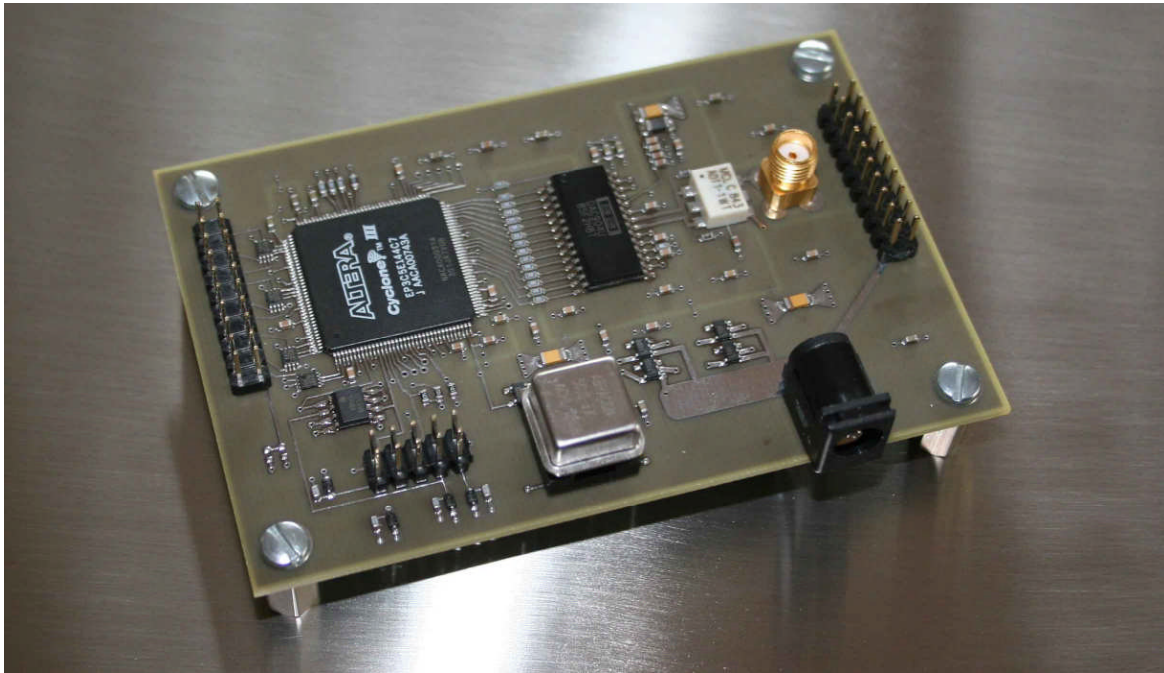


Modulator Evaluation Board

Feb. 2009

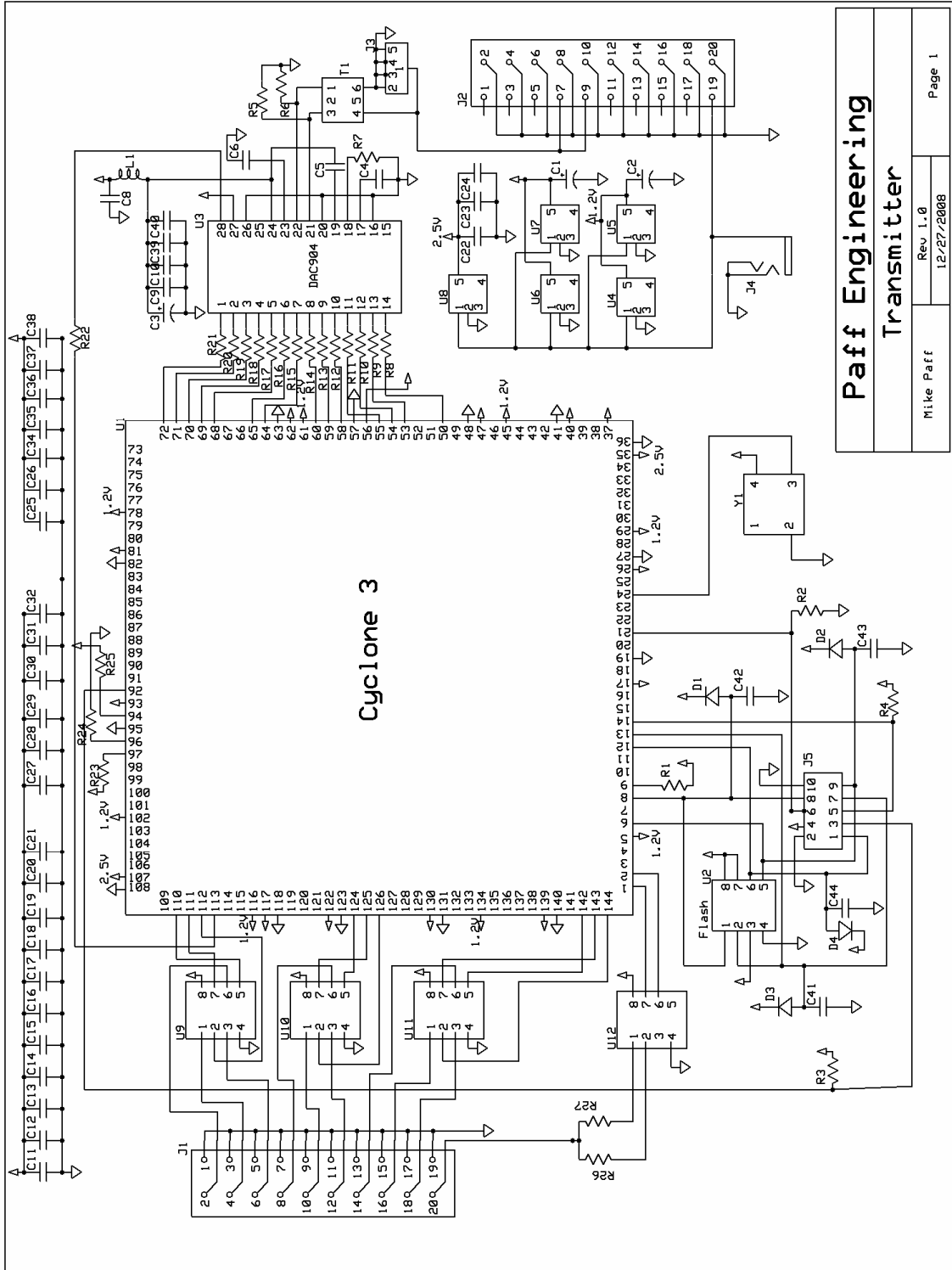


Features

- Low Power Cyclone III technology
- 14 bit DAC904 from Texas Instruments.
- Post DAC transformer insures optimum spectral purity.
- Non Volatile Serial Programming device (EPCS4).
- Buffered IO.
- Input and Output headers to simplify interface testing.
- On board power conditioning requires +5 VDC input.
- Available with or without modulator programmed into FPGA
- Flexible clock frequencies.

This board integrates a small, low power Cyclone III FPGA device with a single, high speed, 14 bit DAC to produce a modulated IF signal used for digital communications. The FPGA may be configured to conform to one of several standards, with a user defined set of operating conditions.

Contact Mike Paff 650 941 2954 (mpaff@paffengineering.com) for more information



Paff Engineering
Transmitter

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