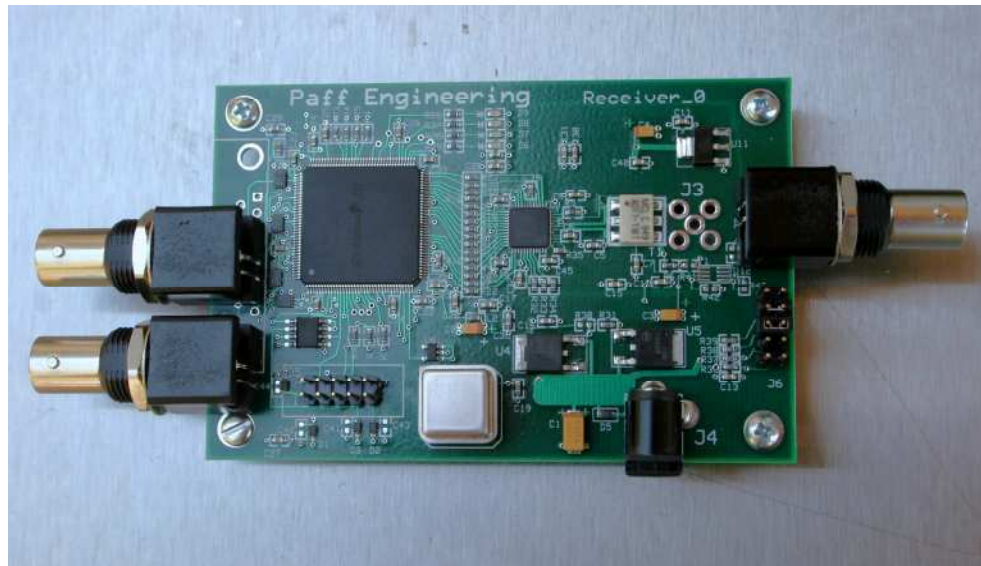


Demodulator Evaluation Board

Aug. 2010

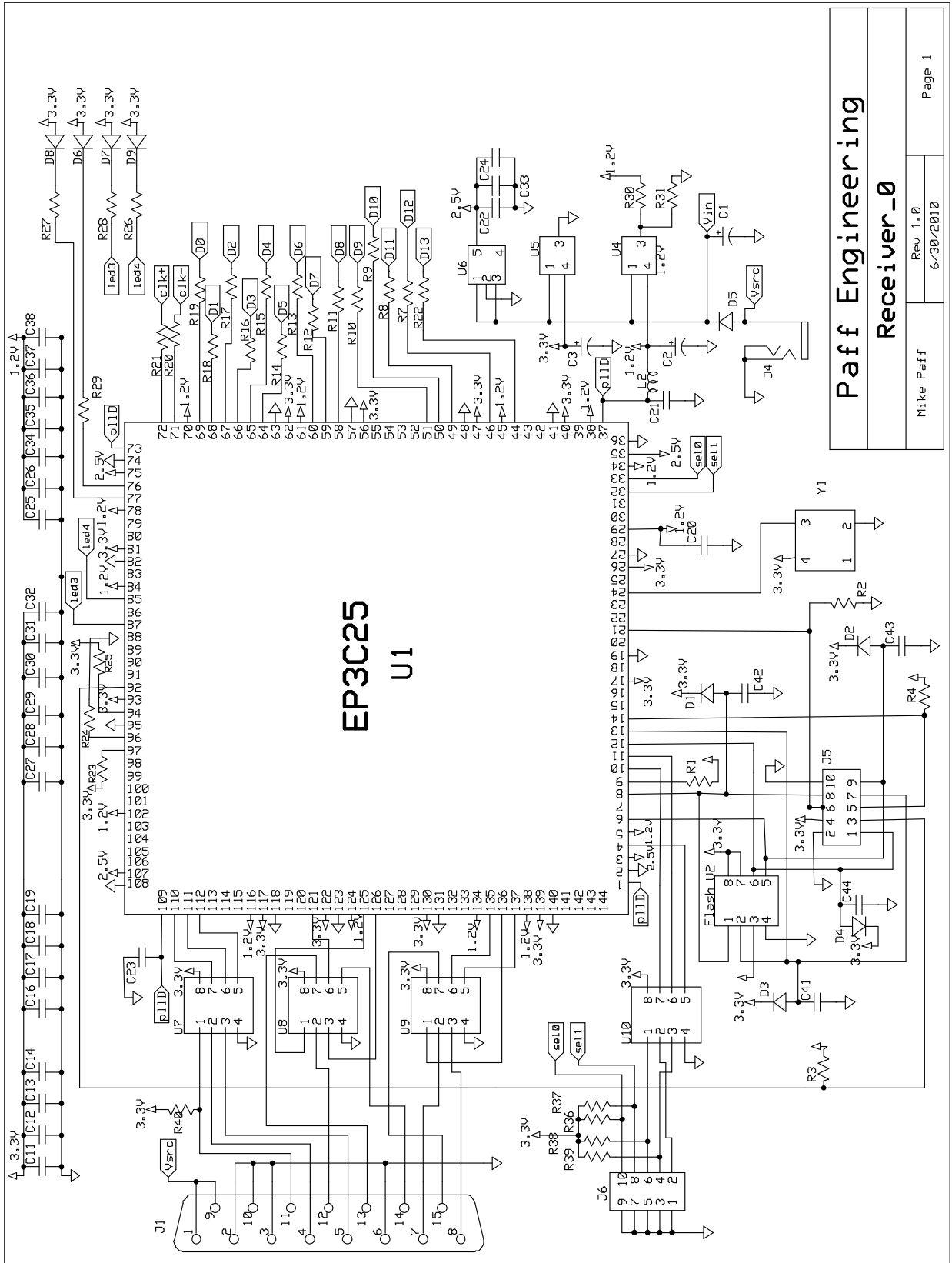


Features

- Low Power Cyclone III technology (EP3C25E144)
- 12 bit A/D converter (AD9233) from Analog Devices.
- Transformer insures spectral purity, with minimum DC bias.
- Op Amp buffer is an option to replace the input transformer in some applications
- Non Volatile Serial Programming device (EPCS16).
- Buffered IO.
- Input and Output header to provide flexible interfaces.
- LED status indicators.
- On board power conditioning requires +5 VDC input.
- 15 Pin D connector may replace standard BNC clock and data output connectors.
- SMA connector may replace standard BNC input connector.
- Flexible clock frequencies.
- Supports ATSC, SOQPSK, OFDM and other modulation formats.

This board integrates a small, low power Cyclone III FPGA device with a single, high speed, 12 bit A/D converter to demodulate an IF signal used for digital communications. The FPGA may be configured to conform to one of several standards, with a user defined set of operating conditions.

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